# TSMC (2330.TW): Communacopia + Technology 2026 — Key Takeaways




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**Bottom Line:** We have three key takeaways: **(1)** TSMC continues to view AI as a multi-year megatrend that remains at a very early stage, with lower compute costs expected to accelerate AI adoption and ultimately drive greater demand for advanced silicon; **(2)** execution in Arizona continues to progress well, with Phase 1 achieving yields comparable to Taiwan and already generating profits, while TSMC expects approximately 30% of its N2-and-beyond capacity to be located outside Taiwan in five years, primarily in Arizona; and **(3)** despite dilution from the N2 ramp and overseas expansion, management remains comfortable achieving its long-term gross margin target of 56% and above, supported by disciplined capacity planning, high utilization, productivity improvements, cost reductions, and pricing that reflects TSMC's value.

## Key Takeaways

- **AI remains in the early stages of a multi-year megatrend:** TSMC continues to see increasing adoption of AI. Management also highlighted the **shift from generative AI toward agentic AI, which should broaden compute demand beyond accelerators to CPUs**. TSMC remains confident in the durability of the AI cycle based on discussions not only with its direct customers but also with its customers' customers. Recent visits of US CSPs further reinforced management's confidence, as cloud service providers appeared well prepared in areas including power and broader infrastructure deployment.
- **Lower compute costs should drive greater consumption and silicon demand:** Management believes **open-source models and continued improvements in computing efficiency will reduce token costs, leading to greater AI consumption and broader adoption, supporting incremental demand for advanced silicon**. TSMC has already incorporated this dynamic into its capacity planning and continues to expand aggressively to support what it views as

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structural, multi-year demand growth.

- **Disciplined demand validation remains critical amid aggressive expansion:** TSMC uses a combination of top-down and bottom-up analysis to validate demand. Its top-down assessment considers macroeconomic conditions, semiconductor demand, fabless/system companies, the foundry industry, etc, while its bottom-up approach evaluates demand across different segments and incorporates feedback from customers and customers' customers. Management acknowledged that semiconductors will remain cyclical, but believes correctly identifying the underlying megatrend is more important than attempting to forecast every short-term fluctuation.
- **Arizona execution is demonstrating that TSMC's manufacturing model can travel:** TSMC reiterated that overseas expansion is based on customer demand for geographic flexibility and an appropriate level of government support. **Arizona Phase 1, using N4 technology, is in operation, with yields comparable to Taiwan, satisfactory quality and reliability, and positive profitability**. Management views this as an important milestone because it demonstrates internally and to customers that TSMC can produce wafers in the US with the same quality as in Taiwan. Equipment move-in for Phase 2, which will use N3 technology, is expected to begin soon; construction of Phase 3 has started; and preparation work for Phase 4 and the first US advanced-packaging facility is underway.
- **Arizona has substantial room for further expansion:** In addition to its original Arizona development, TSMC has acquired another parcel of land **capable of accommodating an additional five to six fabs, supporting its recently announced incremental US$100bn investment**. Based on its current plan, TSMC expects approximately 30% of its N2-and-beyond capacity to be located outside Taiwan in five years, primarily in Arizona. However, the most advanced technologies will continue to ramp first in Taiwan because the early-stage technology transfer requires close collaboration and frequent movement of engineers among R&D and manufacturing sites. Overseas deployment will generally follow after the technology has stabilized in Taiwan.
- **Customers increasingly value and are willing to pay for geographic flexibility:** Management noted that customers initially requested only a modest amount of geographically diversified supply, but their requirements have increased as TSMC has demonstrated successful execution in Arizona. TSMC believes this geographic flexibility represents incremental value and intends to reflect that value in its pricing, with customers increasingly willing to recognize the associated premium.
- **Technology, manufacturing excellence, capacity, and trust underpin TSMC's competitive moat:** Management believes customers select a foundry partner based on access to the best technology, the ability to ramp rapidly into high-volume manufacturing with attractive economics, sufficient capacity, and confidence that the foundry will not compete with its customers. TSMC is confident in maintaining this lead by continuing to advance its roadmap. Management also highlighted its record of rapidly ramping new technologies, the scale of its wafer and advanced-packaging investments, and its consistent delivery against customer

commitments as key elements of customer trust that cannot be replicated through CapEx or engineering recruitment alone.

- **Pricing remains strategic rather than opportunistic:** TSMC does not intend to raise prices simply because near-term utilization is high, nor reduce prices simply because utilization temporarily weakens. Instead, the **company's approach is to price sustainably and consistently based on the value it provides, while ensuring that customers can remain successful over the long term**. Management noted that TSMC's structural profitability has continued to improve, with its long-term gross margin target increasing from 50% five years ago to 53% and now 56% and above.
- **Leading-edge wafer and advanced-packaging capacity remain tight:** Management continues to build capacity wherever possible to meet customer demand. N3 demand remains particularly strong: unlike prior technology cycles, when demand for a node typically began declining two to three years after its introduction, **TSMC is adding further N3 capacity and still does not expect supply to be sufficient**. The company is also expanding advanced-packaging capacity to address strong accelerator demand. Outside TSMC, management identified power and memory as potential bottlenecks cited by other parts of the AI supply chain, while emphasizing that foundry capacity itself remains difficult to expand because fabs require two to three years to build and another one to two years to ramp.
- **TSMC's disclosed AI revenue understates its broader exposure:** Management reiterated that its disclosed AI revenue definition is relatively narrow and primarily captures GPUs, custom AI accelerators and memory base die. It excludes CPUs and networking products because TSMC cannot reliably determine whether each chip is ultimately deployed in an AI system. As agentic AI drives incremental demand across x86, Arm, and RISC-V CPUs, as well as networking and other supporting silicon, TSMC's total AI-related exposure is therefore broader than its reported AI-accelerator revenue. The company intends to retain its existing definition until it can more accurately distinguish the end use of these products.
- **No visible barrier to continued transistor scaling:** TSMC is currently ramping N2, with A14 expected approximately two years later, while development of the subsequent node is already underway. Management does not currently see a technical issue preventing delivery of the next node or continued advancement beyond the existing roadmap. TSMC views close collaboration with customers as a key foundry advantage, allowing the company to develop each technology against specific customer performance, power, density, and economic requirements.
- **Cross-node capacity optimization is an underappreciated advantage:** TSMC highlighted its ability to generate additional output through productivity improvements and to convert tools and capacity across technology nodes. For example, the company can use portions of its N5 capacity to support N3 demand and can also optimize capacity across less-adjacent nodes where technically feasible. Management views this **cross-node flexibility as a distinctive element of TSMC's manufacturing excellence, allowing the company to respond more quickly to demand changes while reducing the risk of capacity obsolescence**.
- **Advanced packaging is strategic primarily because it enables wafer growth:**

CoWoS capacity remains tight, and TSMC already started outsourcing selected packaging processes to OSAT partners to support additional growth. Management emphasized that **advanced packaging is intended primarily to ensure and enable wafer demand rather than serve as a standalone profit pool**. TSMC is therefore developing multiple packaging architectures, including panel-level packaging, etc, while remaining open to customers using alternative packaging providers, as either approach ultimately supports demand for TSMC wafers.

- **Advanced-packaging execution remains a key differentiator:** TSMC noted that its current advanced-packaging capability supports approximately 5.5x reticle size, with a roadmap to reach approximately 14x reticle sizes within the next several years. While competing roadmaps may also appear attractive, management believes execution and the ability to deliver in high-volume manufacturing will ultimately determine competitive outcomes. **TSMC does not believe competitors can use advanced-packaging wins to meaningfully displace its leading-edge wafer business, as foundry and packaging remain fundamentally different businesses**.
- **A unified TSMC culture supports overseas manufacturing execution:** TSMC's objective is not to create separate Taiwan and US cultures, but to establish a consistent TSMC culture centered on integrity, commitment, innovation, and customer trust (ICIC). Locally hired Arizona employees spend six to eighteen months training and working in Taiwan, while experienced Taiwan-based employees are assigned to Arizona to work alongside local teams. Over time, the number of Taiwan assignees should decline as locally trained employees assume greater responsibility. TSMC is applying this approach across its overseas manufacturing sites.
- **Management remains comfortable with its 56%-and-above long-term gross margin target:** TSMC reiterated its 6 major drivers of profitability: new-technology ramp, pricing, cost, product mix, utilization, and FX. The N2 ramp is expected to dilute 2026 GM by approximately 2-3ppts, but its profitability should improve over time toward the corporate average; management also noted that N2's structural profitability is better than N3 at a comparable stage. Overseas expansion is expected to dilute GM by 2-3ppts during the early portion of the current five-year period, then widening to 3-ppts later as more overseas fabs begin production. Each new fab will initially create dilution before improving along its learning curve. Taking these puts and takes together, **management remains very comfortable that disciplined capacity planning, healthy utilization, cost reduction, productivity gains, and pricing that reflects TSMC's value will enable the company to deliver for shareholders and achieve its long-term gross margin target 'of 56% and higher' with focus on the 'higher' side**.

## Valuation/Risks

**TSMC (Buy)**: Our 12-month price target is NT$3,100, which is derived by applying a target P/E multiple of 22x to our 2027E EPS. Our 22x target P/E is benchmarked against 1.0stdv above its 5-year trading average. For the ADR (TSM), we have a 12-month price target of US$620, based on a USD/TWD rate of 30.0 and an ADR premium of 20%. **Key**

**downside risks to our views:** (1) further deterioration in end-demand recovery impacting capacity utilization; (2) slower customer node migrations; (3) slowdown in AI investment resulting in lower long-term semiconductor content growth; (4) poor yields/execution resulting in worse-than-expected profitability; (5) stronger competition resulting in ASP/profitability erosion; and (6) unfavorable FX trend or higher-than-expected cost increase weighing on the margin outlook.

| | | | |
|---|---|---|---|
| **2330.TW** | 12m Price Target: **NT$3,100.00** | Price: **NT$2,465.00** | Upside: **25.8%** |
| **TSM** | 12m Price Target: **$620.00** | Price: **$435.36** | Upside: **42.4%** |

**Buy**

Market cap: NT$63.9tr / $2.0tr
Enterprise value: NT$61.2tr / $1.9tr
3m ADTV: NT$74.0bn / $2.3bn
Taiwan
Taiwan Semiconductor
M&A Rank: 3
Leases incl. in net debt & EV?: Yes

## GS Forecast

| | 12/25 | 12/26E | 12/27E | 12/28E |
|---|---|---|---|---|
| Revenue (NT$ mn) | 3,809,054.3 | 5,532,239.7 | 7,343,151.2 | 9,393,969.4 |
| EBITDA (NT$ mn) | 2,624,188.0 | 4,086,905.6 | 5,489,197.9 | 7,056,258.1 |
| EPS (NT$) | 66.26 | 109.21 | 140.91 | 180.83 |
| P/E (X) | 17.6 | 22.6 | 17.5 | 13.6 |
| P/B (X) | 5.6 | 8.3 | 6.1 | 4.5 |
| Dividend yield (%) | 1.9 | 1.2 | 1.3 | 1.5 |
| N debt/EBITDA (ex lease,X) | (0.7) | (0.7) | (0.8) | (1.1) |
| CROCI (%) | 24.3 | 34.2 | 37.8 | 40.8 |
| FCF yield (%) | 3.3 | 2.3 | 3.9 | 5.9 |

| | 6/26 | 9/26E | 12/26E | 3/27E |
|---|---|---|---|---|
| EPS (NT$) | 27.25 | 28.06 | 31.82 | 31.38 |

Source: Company data, Goldman Sachs Research estimates, FactSet. Price as of 9 Sep 2026 close.

# Disclosure Appendix

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Financial Returns and Multiple use the Goldman Sachs analystorecasts at the fiscal year-end at least three quarters in theuture. Growth uses inputs or the fiscal year at least seven quarters in theuture compared with the year at least three quarters in theuture (on a per-share basisor all metrics).

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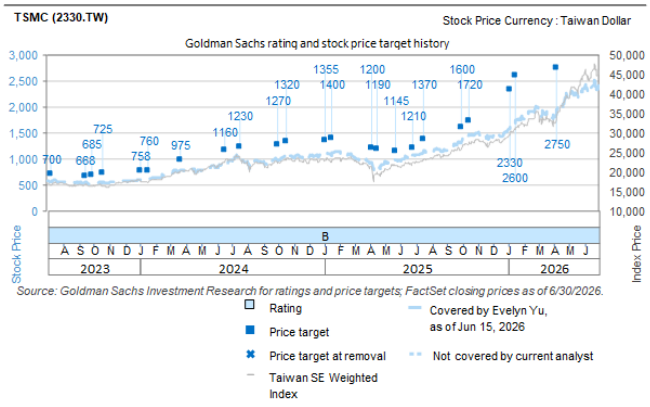
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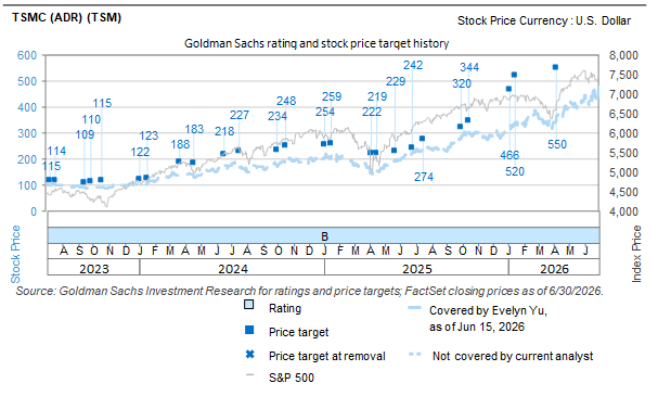
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|---|---|---|---|---|---|---|
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## Target price history table(s)

### TSMC (2330.TW)

| Date of report | Target price (NT$) | Closing price (NT$) |
|---|---|---|
| 16-Jul-26 | 3,100.00 | 2,470.00 |
| 02-Jul-26 | 3,000.00 | 2,465.00 |
| 07-Apr-26 | 2,750.00 | 1,860.00 |
| 15-Jan-26 | 2,600.00 | 1,690.00 |
| 04-Jan-26 | 2,330.00 | 1,585.00 |
| 16-Oct-25 | 1,720.00 | 1,485.00 |
| 30-Sep-25 | 1,600.00 | 1,305.00 |
| 17-Jul-25 | 1,370.00 | 1,130.00 |
| 25-Jun-25 | 1,210.00 | 1,070.00 |
| 22-May-25 | 1,145.00 | 982.00 |
| 17-Apr-25 | 1,190.00 | 847.00 |
| 06-Apr-25 | 1,200.00 | 942.00 |
| 16-Jan-25 | 1,400.00 | 1,105.00 |
| 03-Jan-25 | 1,355.00 | 1,075.00 |
| 17-Oct-24 | 1,320.00 | 1,035.00 |
| 30-Sep-24 | 1,270.00 | 957.00 |
| 18-Jul-24 | 1,230.00 | 1,005.00 |
| 18-Jun-24 | 1,160.00 | 943.00 |
| 21-Mar-24 | 975.00 | 784.00 |
| 18-Jan-24 | 760.00 | 588.00 |
| 02-Jan-24 | 758.00 | 593.00 |
| 19-Oct-23 | 725.00 | 546.00 |
| 28-Sep-23 | 685.00 | 523.00 |
| 14-Sep-23 | 668.00 | 550.00 |

### TSMC (ADR) (TSM)

| Date of report | Target price ($) | Closing price ($) |
|---|---|---|
| 16-Jul-26 | 620.00 | 409.74 |
| 02-Jul-26 | 600.00 | 434.16 |
| 07-Apr-26 | 550.00 | 345.32 |
| 15-Jan-26 | 520.00 | 341.64 |
| 04-Jan-26 | 466.00 | 319.61 |
| 16-Oct-25 | 344.00 | 299.84 |
| 30-Sep-25 | 320.00 | 279.29 |
| 17-Jul-25 | 274.00 | 245.60 |
| 25-Jun-25 | 242.00 | 222.74 |
| 22-May-25 | 229.00 | 196.19 |
| 17-Apr-25 | 219.00 | 151.74 |
| 06-Apr-25 | 222.00 | 146.80 |
| 16-Jan-25 | 259.00 | 214.79 |
| 03-Jan-25 | 254.00 | 208.61 |
| 17-Oct-24 | 248.00 | 205.84 |
| 30-Sep-24 | 234.00 | 173.67 |
| 18-Jul-24 | 227.00 | 171.87 |
| 18-Jun-24 | 218.00 | 179.69 |
| 18-Apr-24 | 183.00 | 132.27 |
| 21-Mar-24 | 188.00 | 139.45 |
| 18-Jan-24 | 123.00 | 113.03 |
| 02-Jan-24 | 122.00 | 101.53 |
| 19-Oct-23 | 115.00 | 92.91 |
| 28-Sep-23 | 110.00 | 86.41 |
| 14-Sep-23 | 109.00 | 91.47 |

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